

FIG. 1

FIG. 1

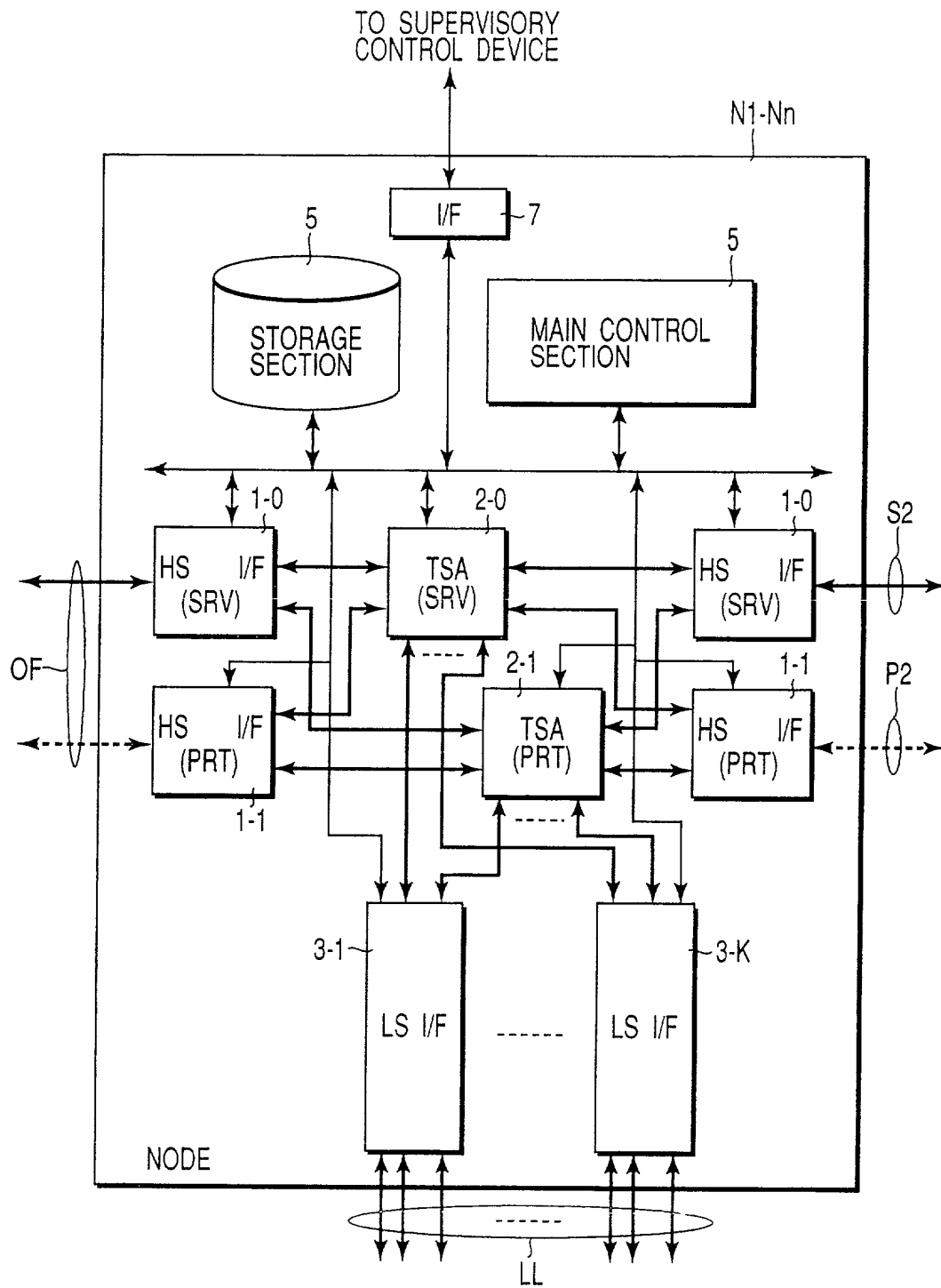


FIG. 2

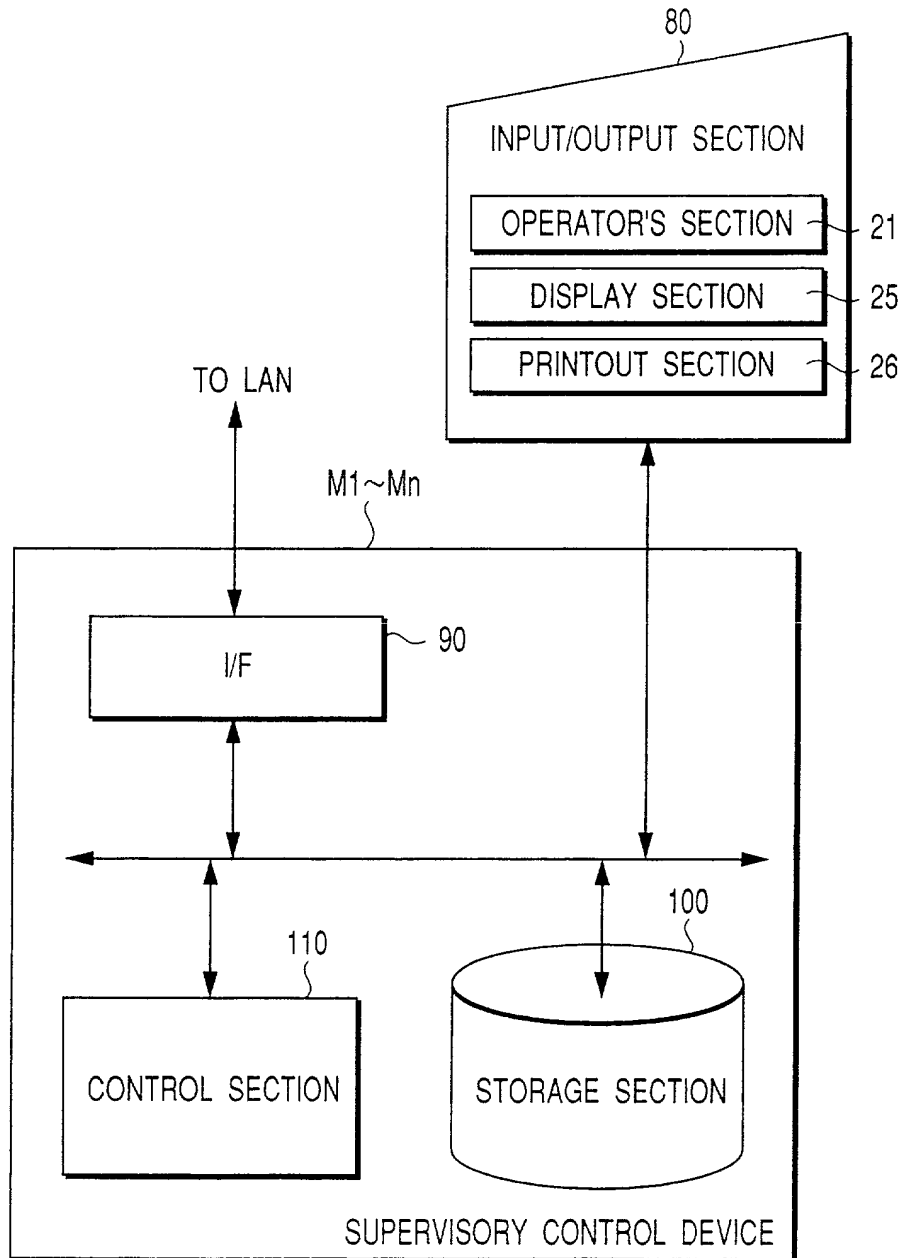


FIG. 3

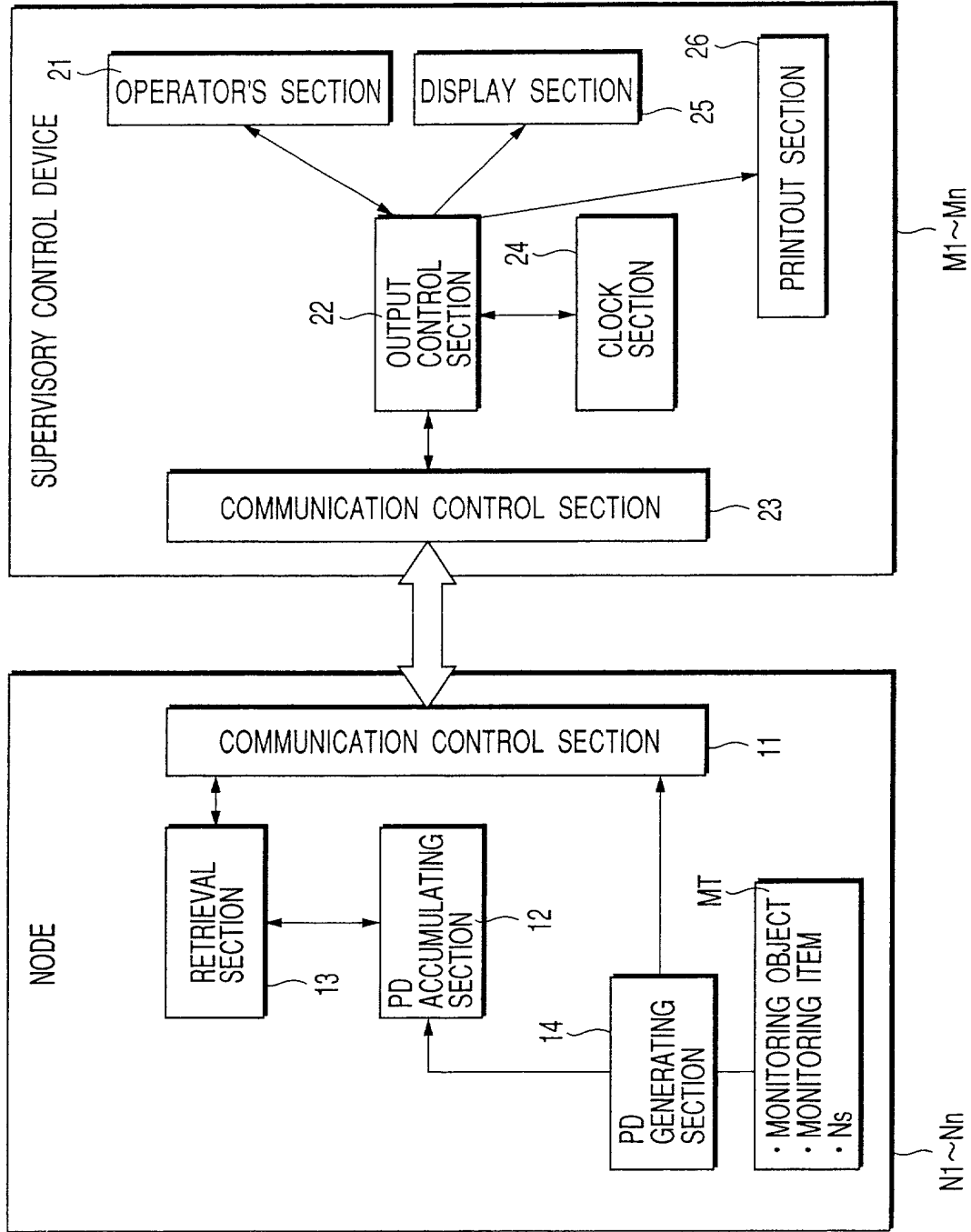


FIG. 4

FIG. 4

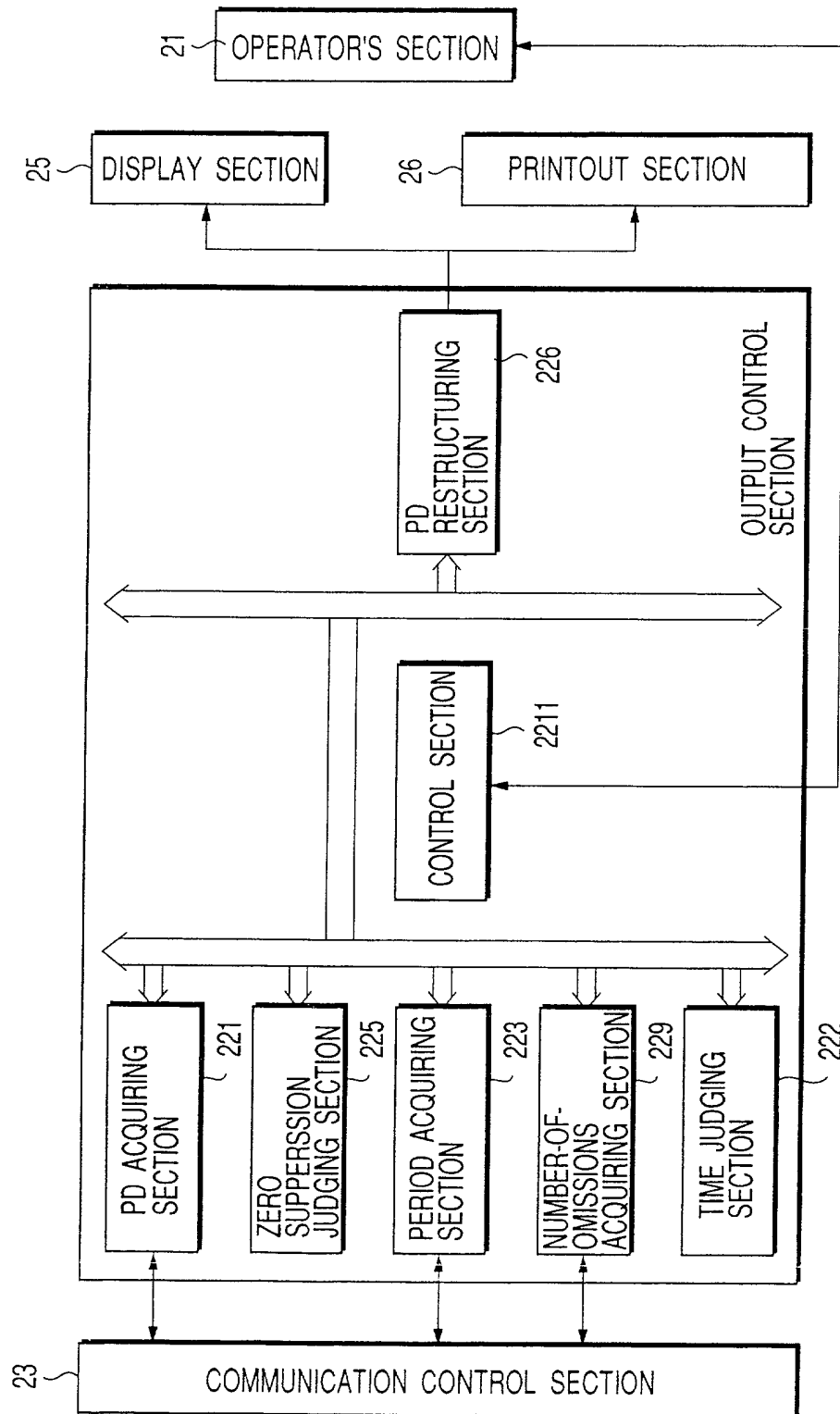


FIG. 5

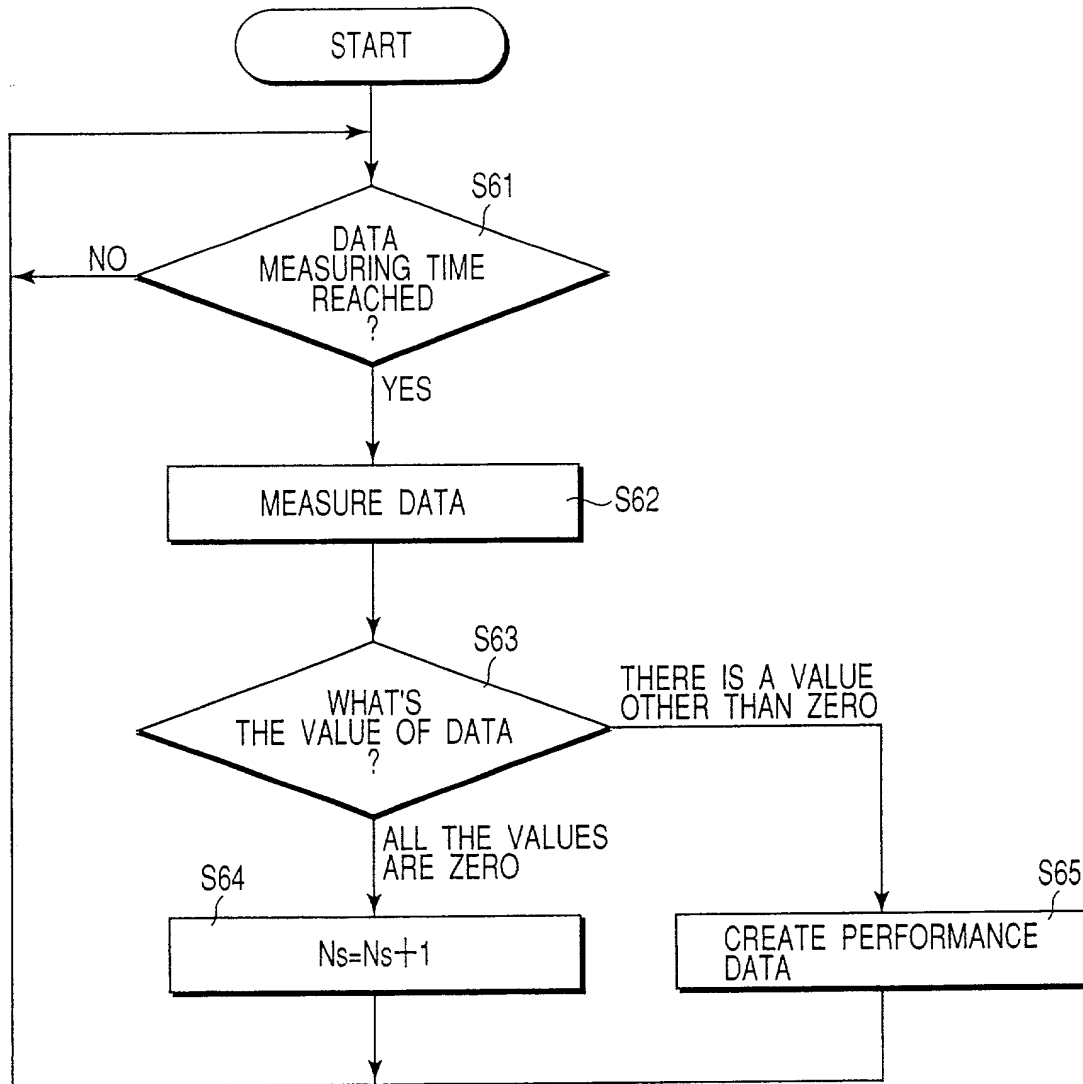


FIG. 6

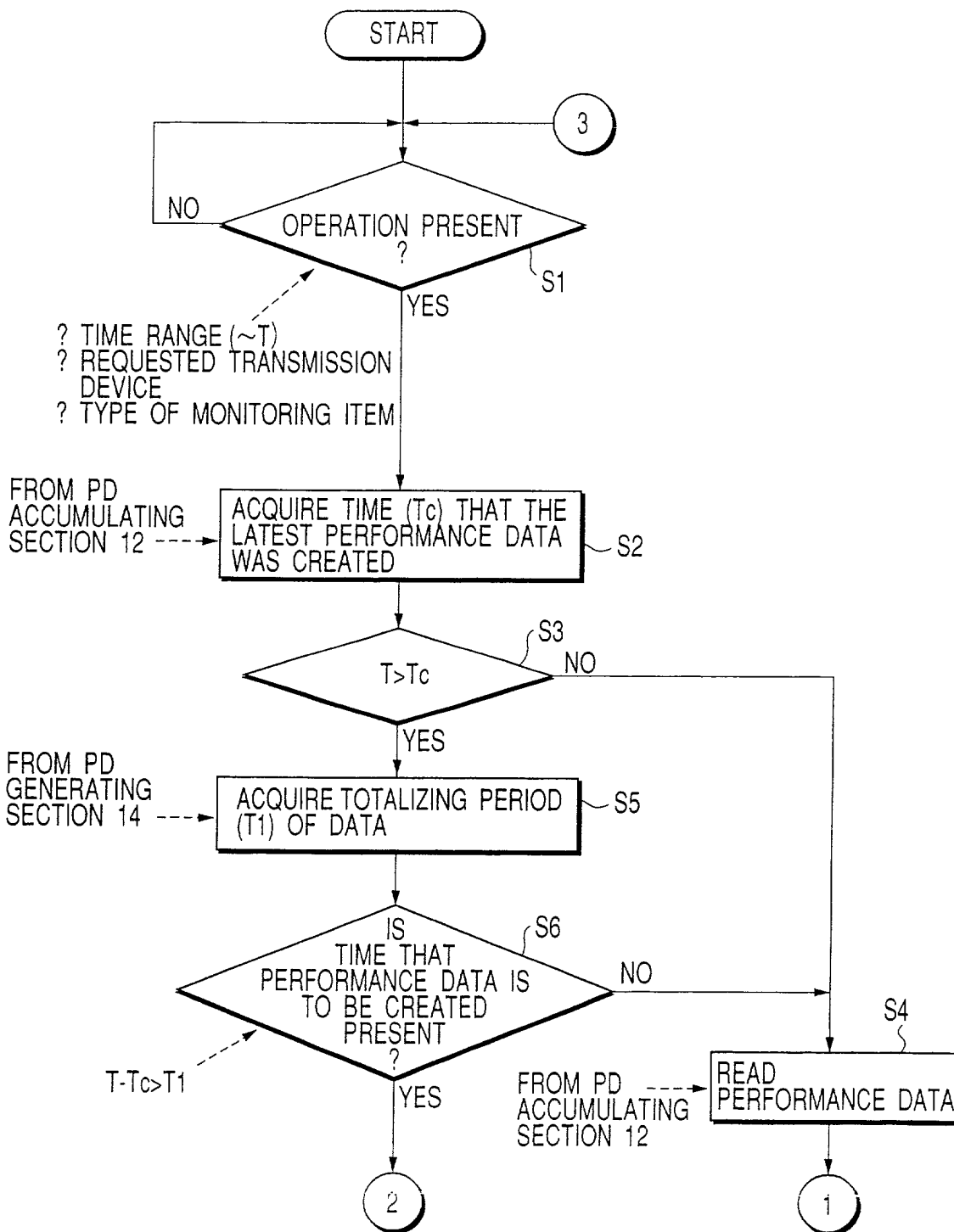


FIG. 7A

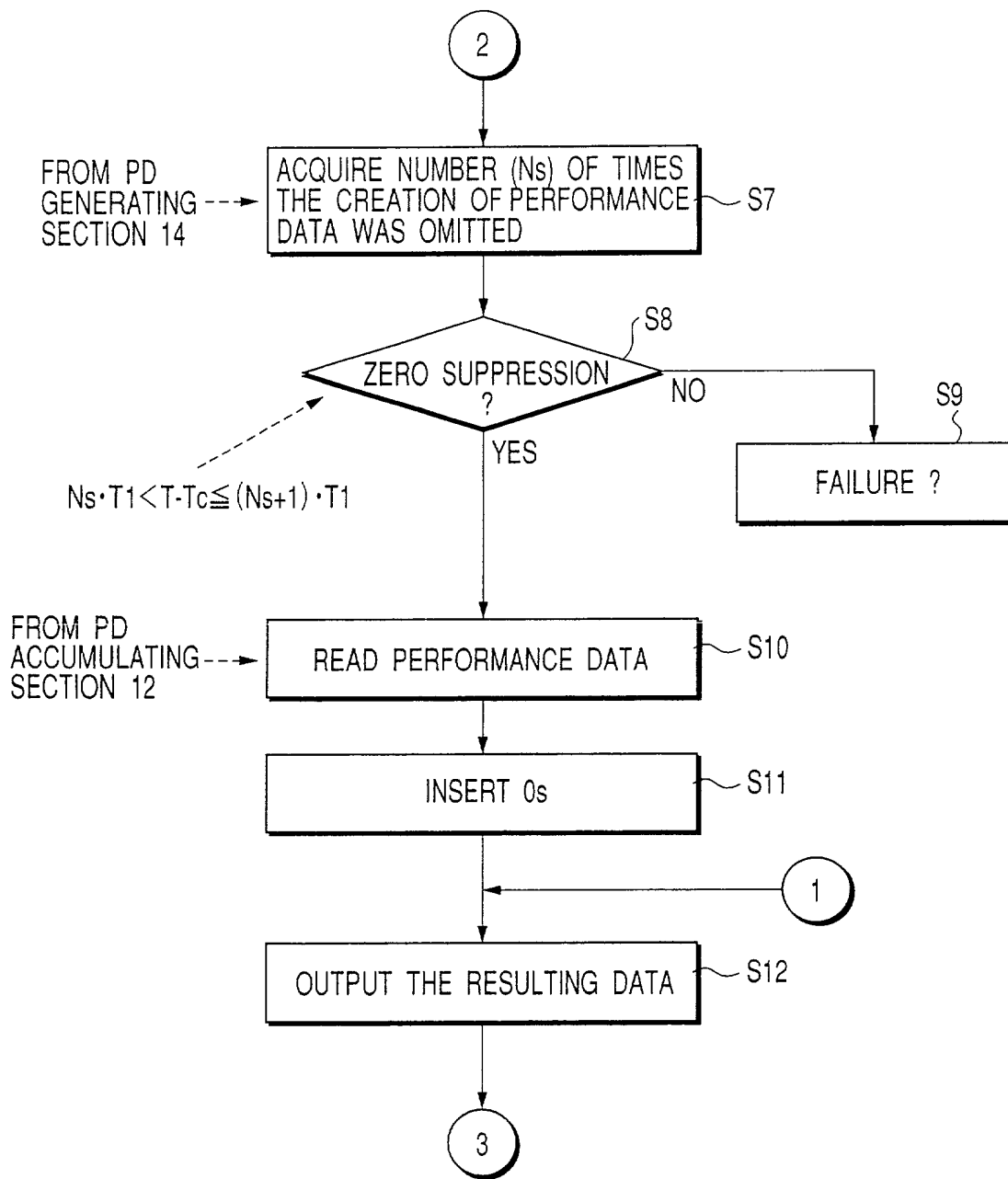


FIG. 7B

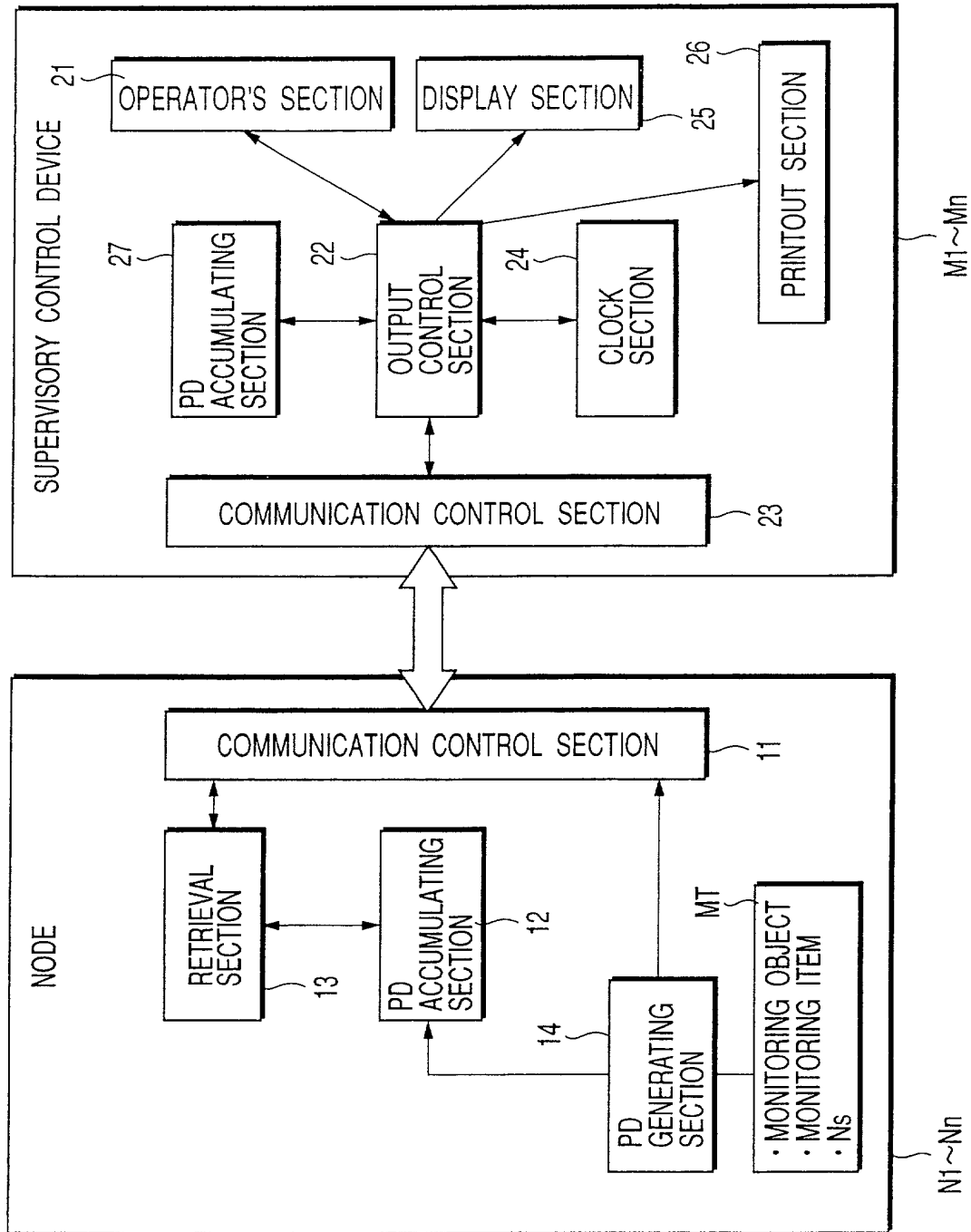


FIG. 8

FIG. 8

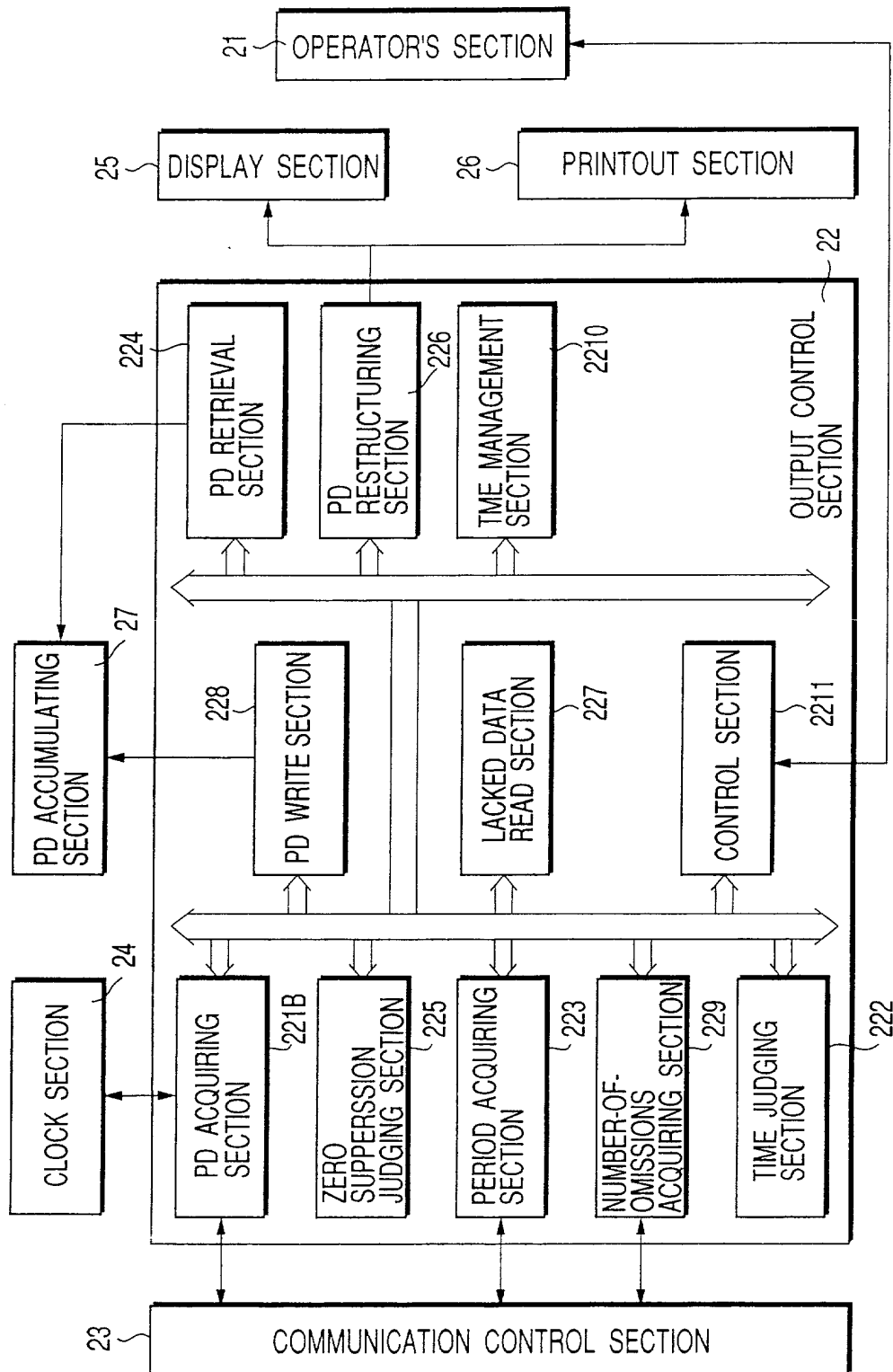


FIG. 9

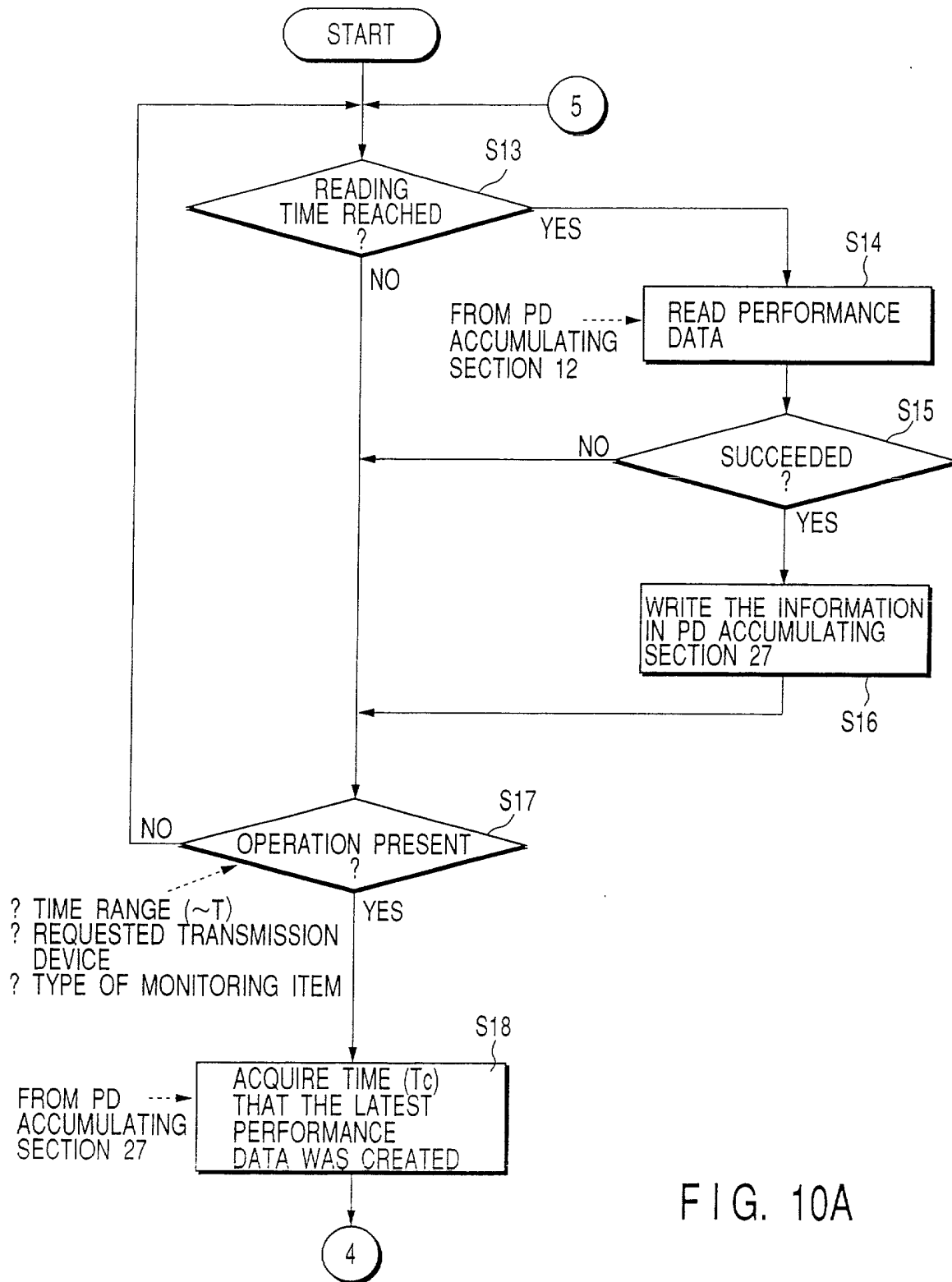
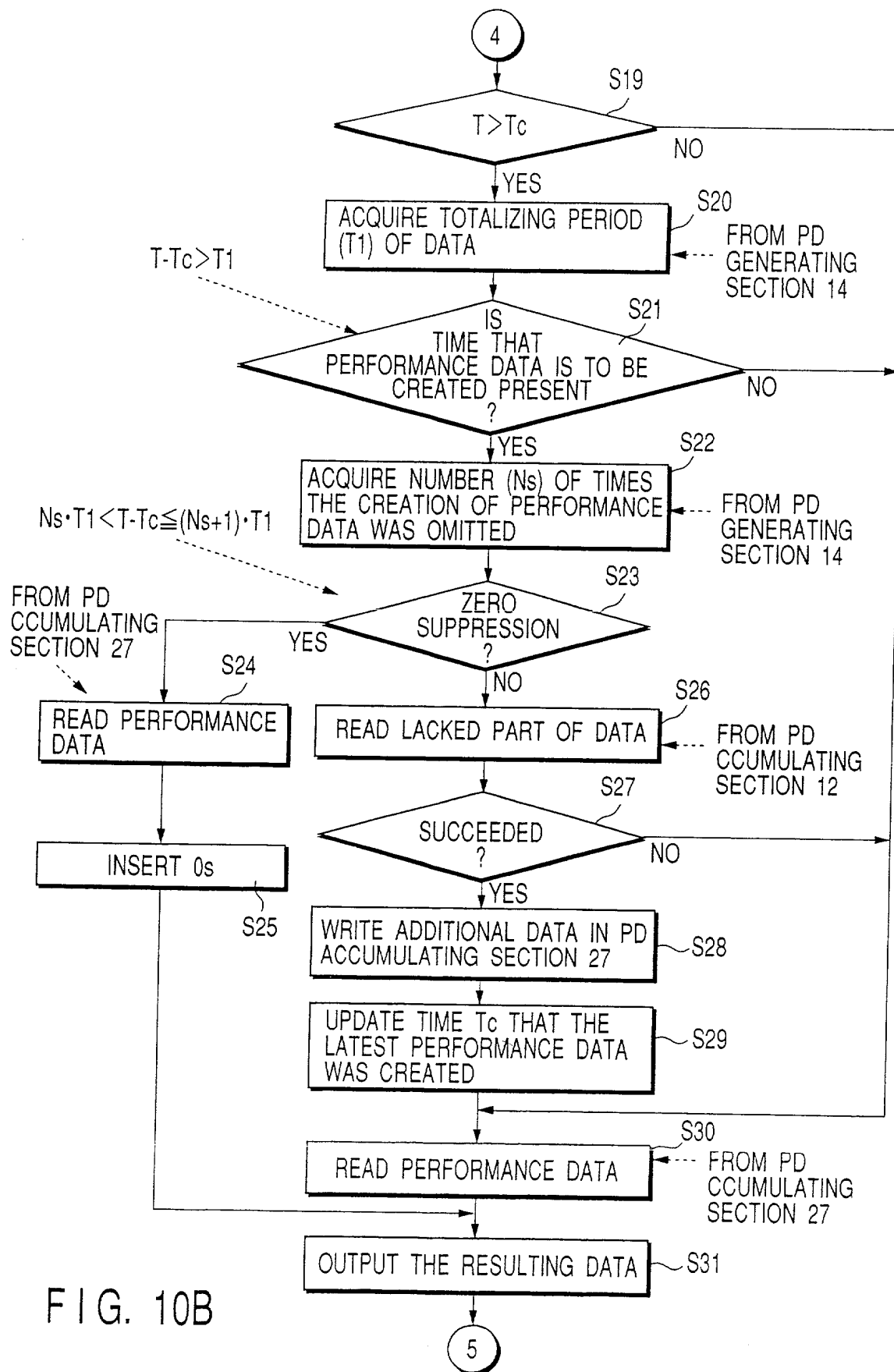


FIG. 10A



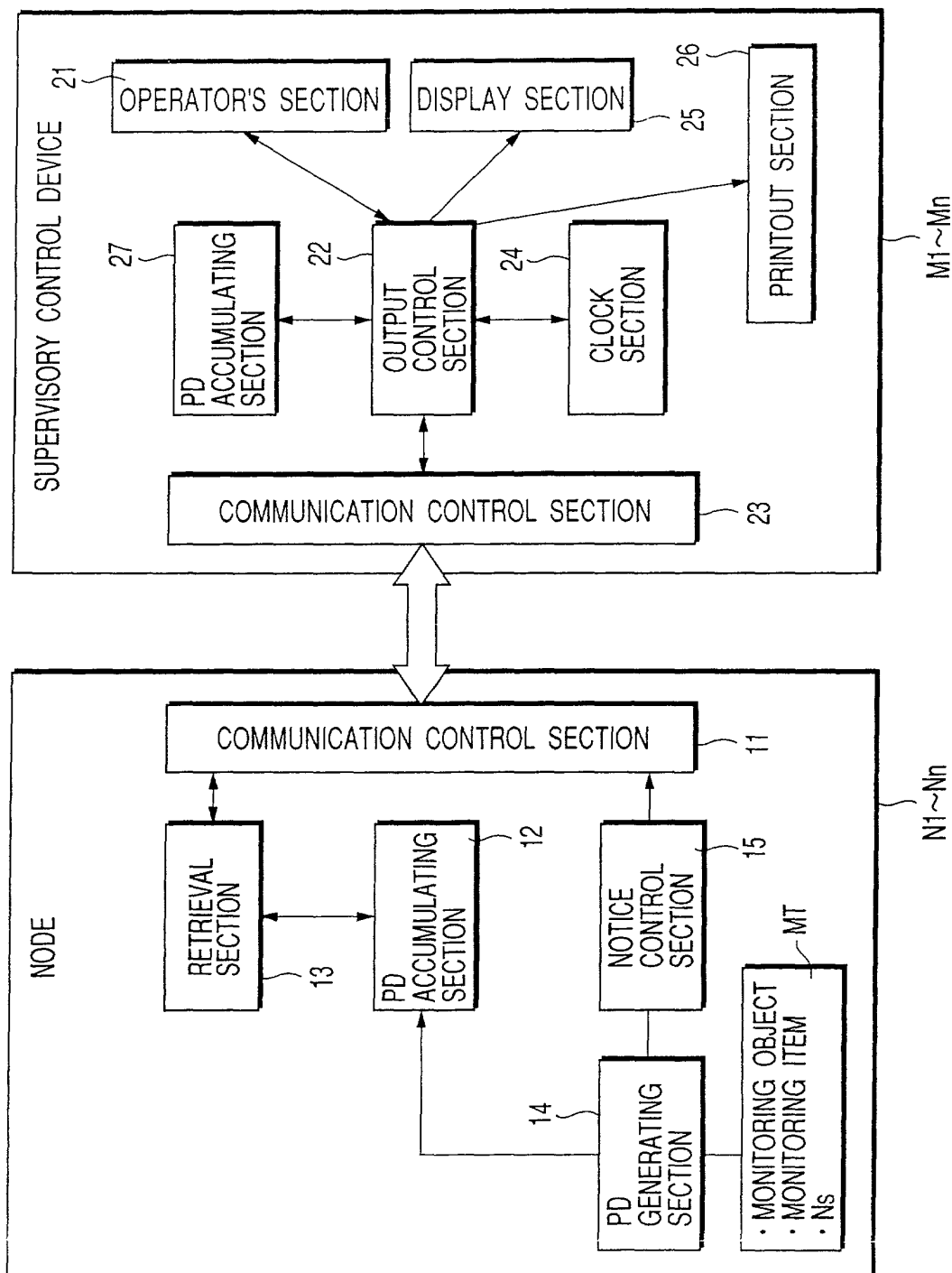


FIG. 11

FIG. 11

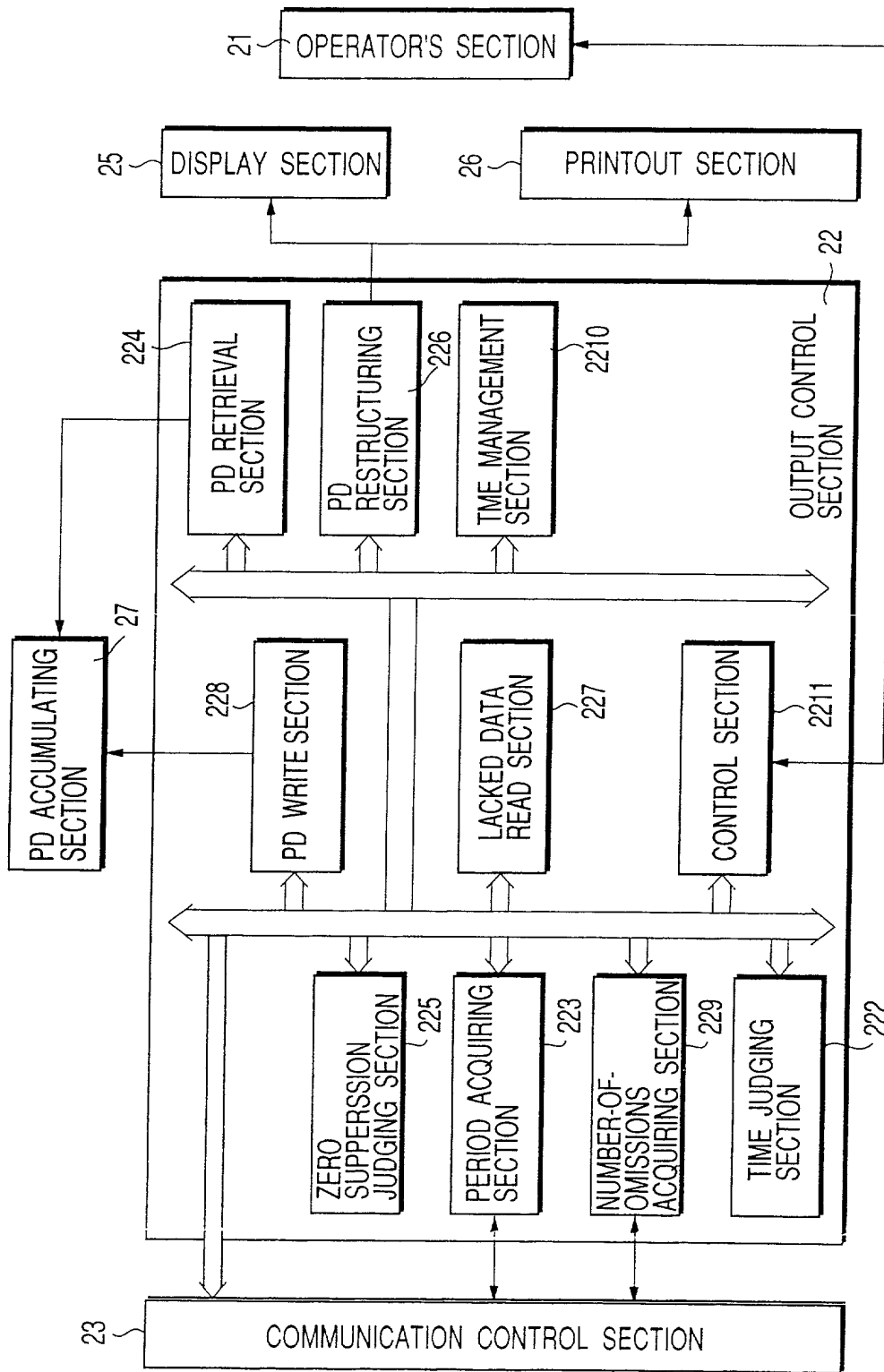


FIG. 12

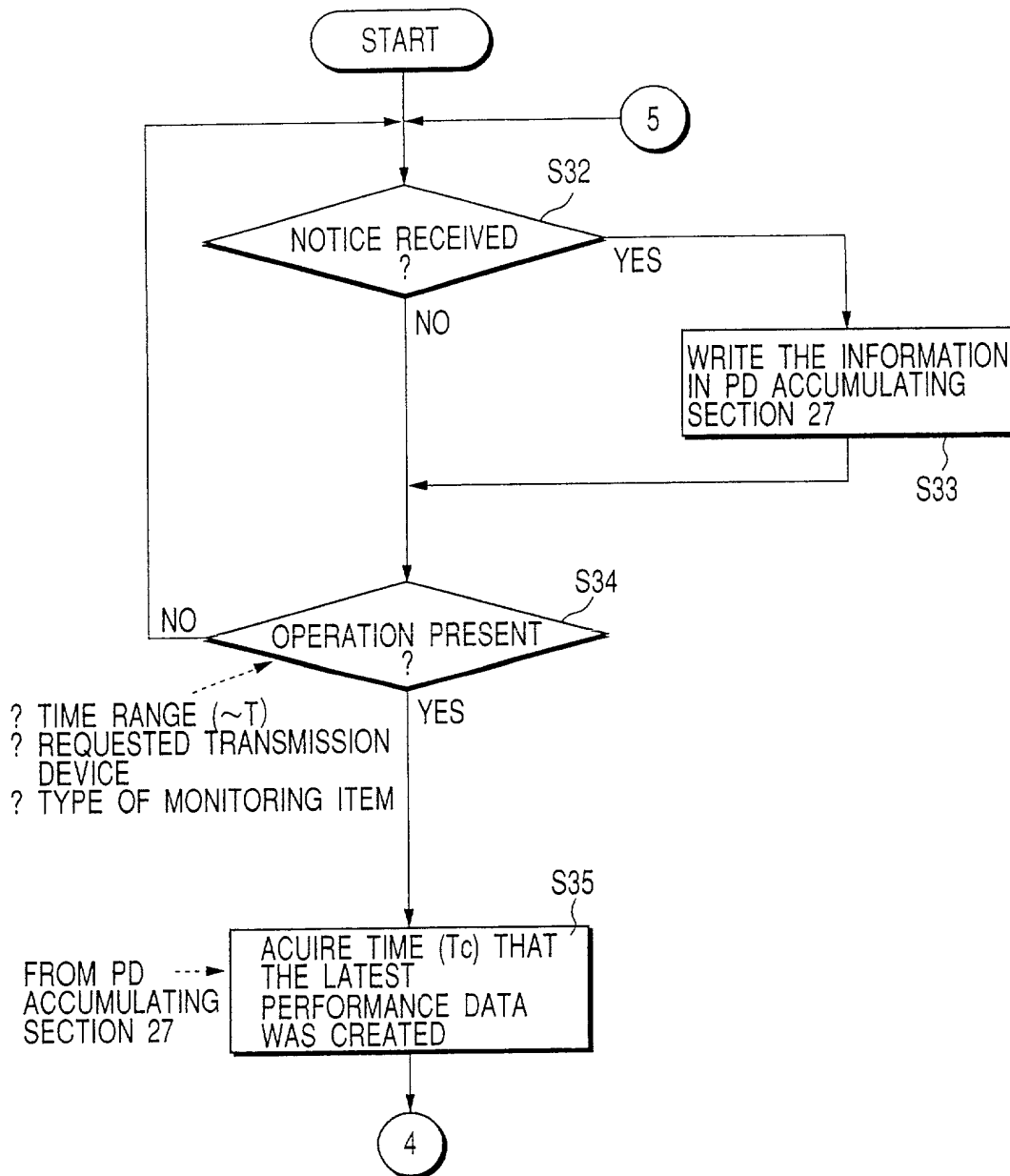


FIG. 13

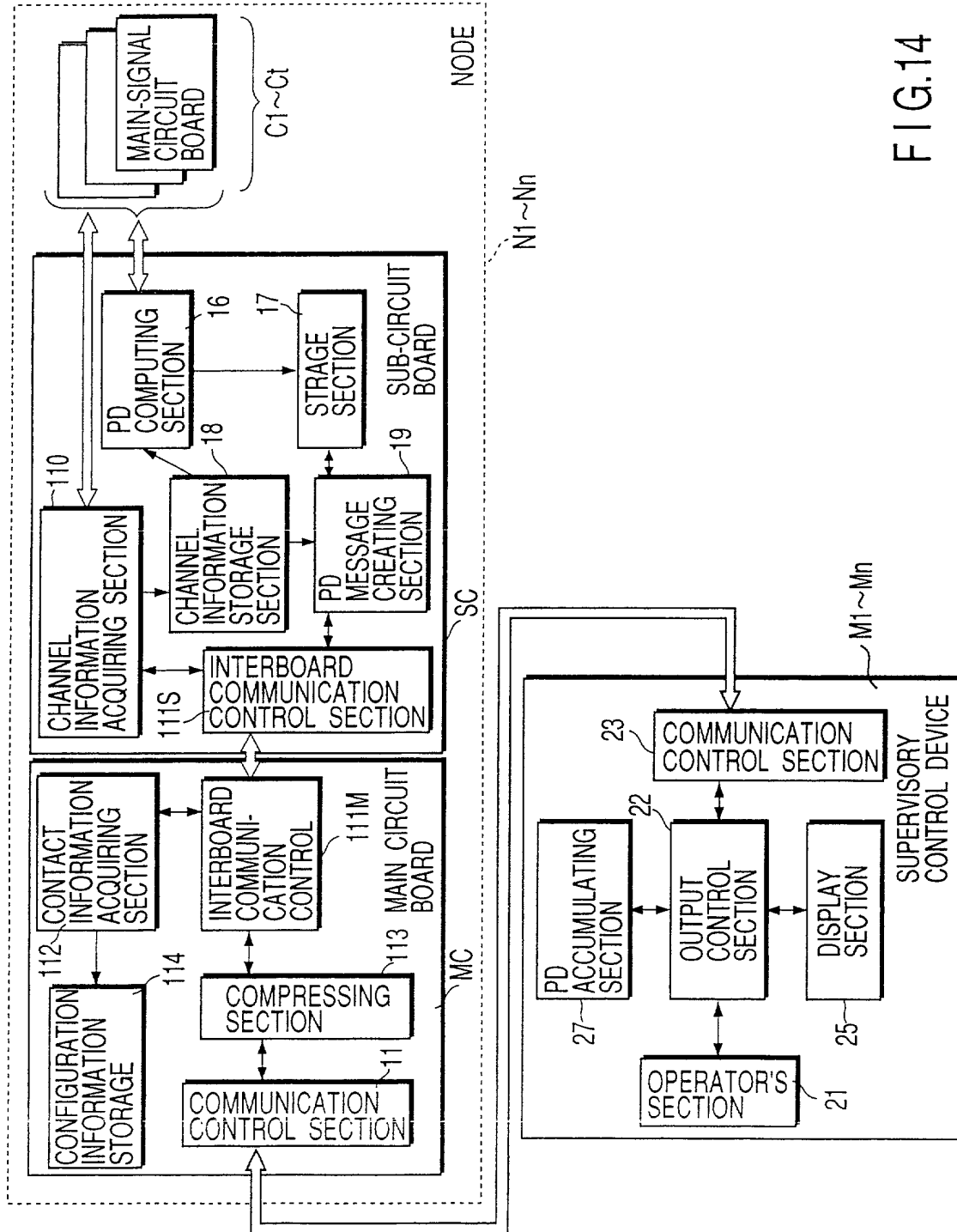


FIG.14

FIG.15

QUALITY DATA (RS) ON Ch1
QUALITY DATA (MS) ON Ch1
QUALITY DATA (PS) ON Ch1
QUALITY DATA (RS) ON Ch2
...

17: PD STORAGE SECTION

FIG.16

COMPRESSION INFORMATION (0: ABSENT/F: PRESENT)			
INFORMATION ON Ch 1	APS CREATE (0: ABSENT/ 1: PRESENT)	INSTALLATION (0: ABSENT/ 1: PRESENT)	VALUE 1: STM-1 2: STM-4 4: STM-16 8: STM-64 TYPE OF CIRCUIT BOARD
INFORMATION ON Ch 2			
...			

18: CHANNEL INFORMATION
STORAGE SECTION

FIG.17

COMPRESSION INFORMATION (0: ABSENT/F: PRESENT)			
INFORMATION ON SHELF 1	COMPRESSION INFORMATION (0: ABSENT/ 1: PRESENT)	ABSENCE OR PRESENCE OF SHELF (0: ABSENT/ 1: PRESENT)	VALUE 1: STM-1 2: STM-4 4: STM-16 8: STM-64 TYPE OF CIRCUIT BOARD
...			

114: CONFIGURATION INFORMATION
STORAGE SECTION

09788593 022404
107220 2588260

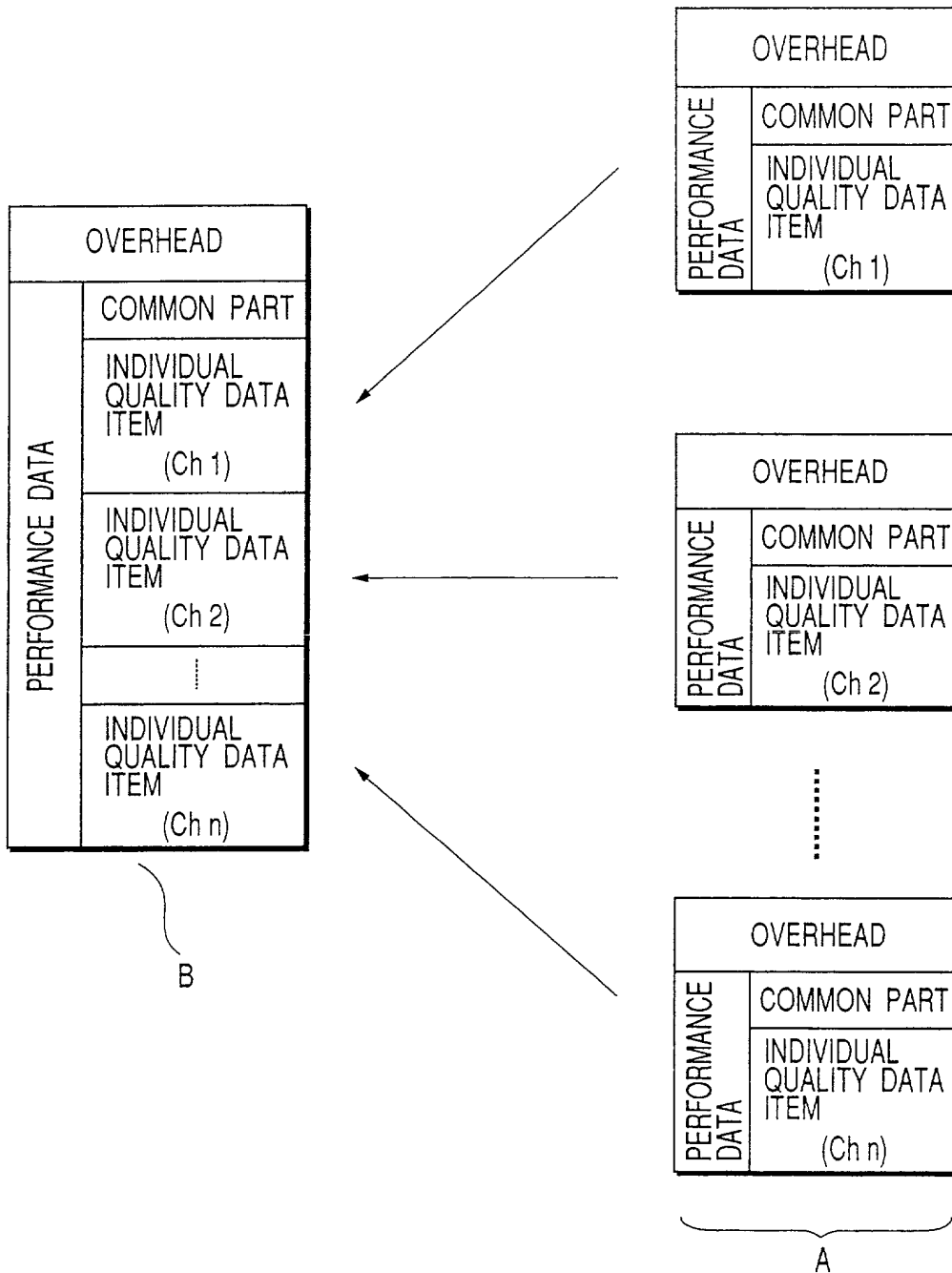


FIG.18

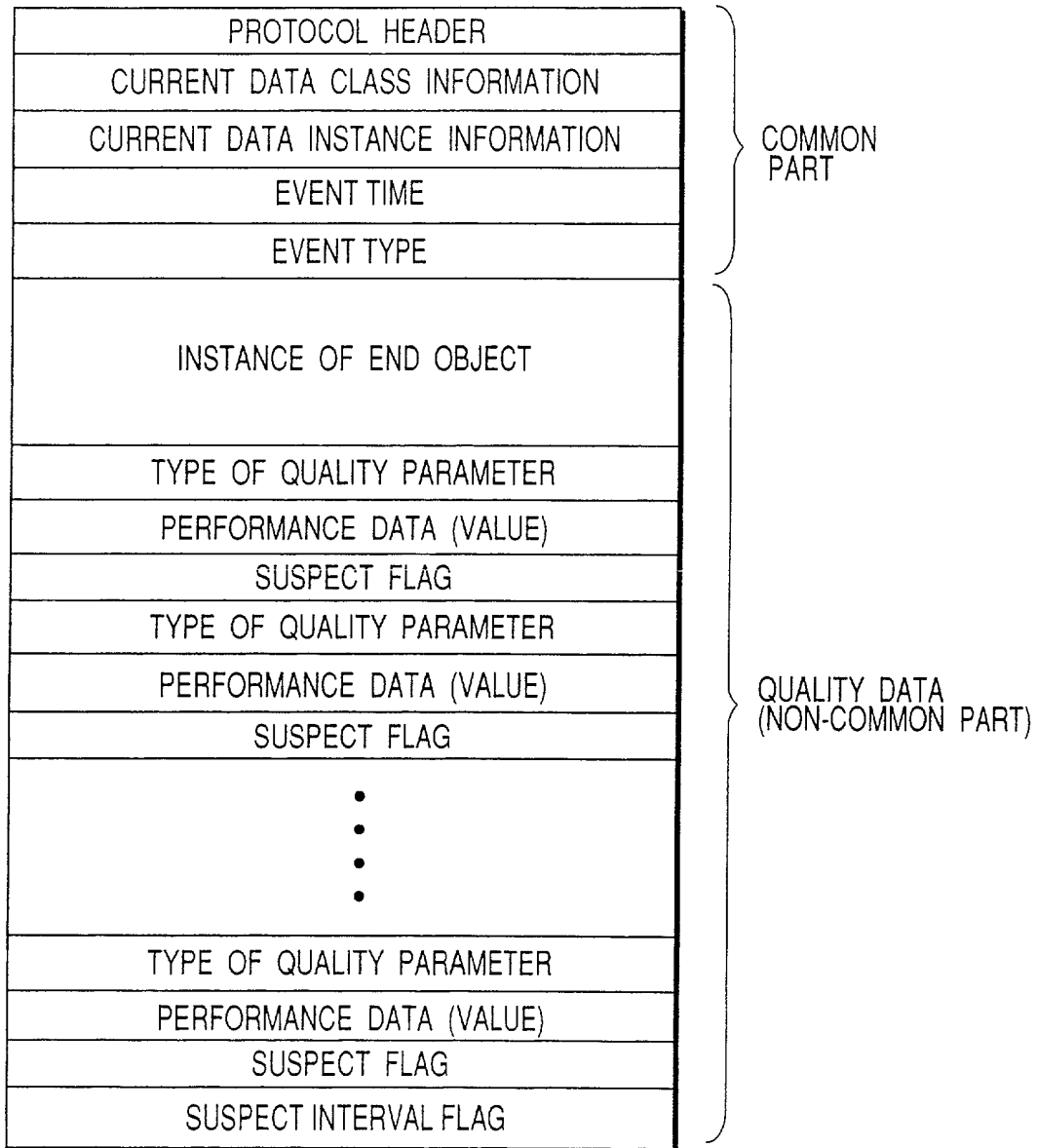


FIG.19

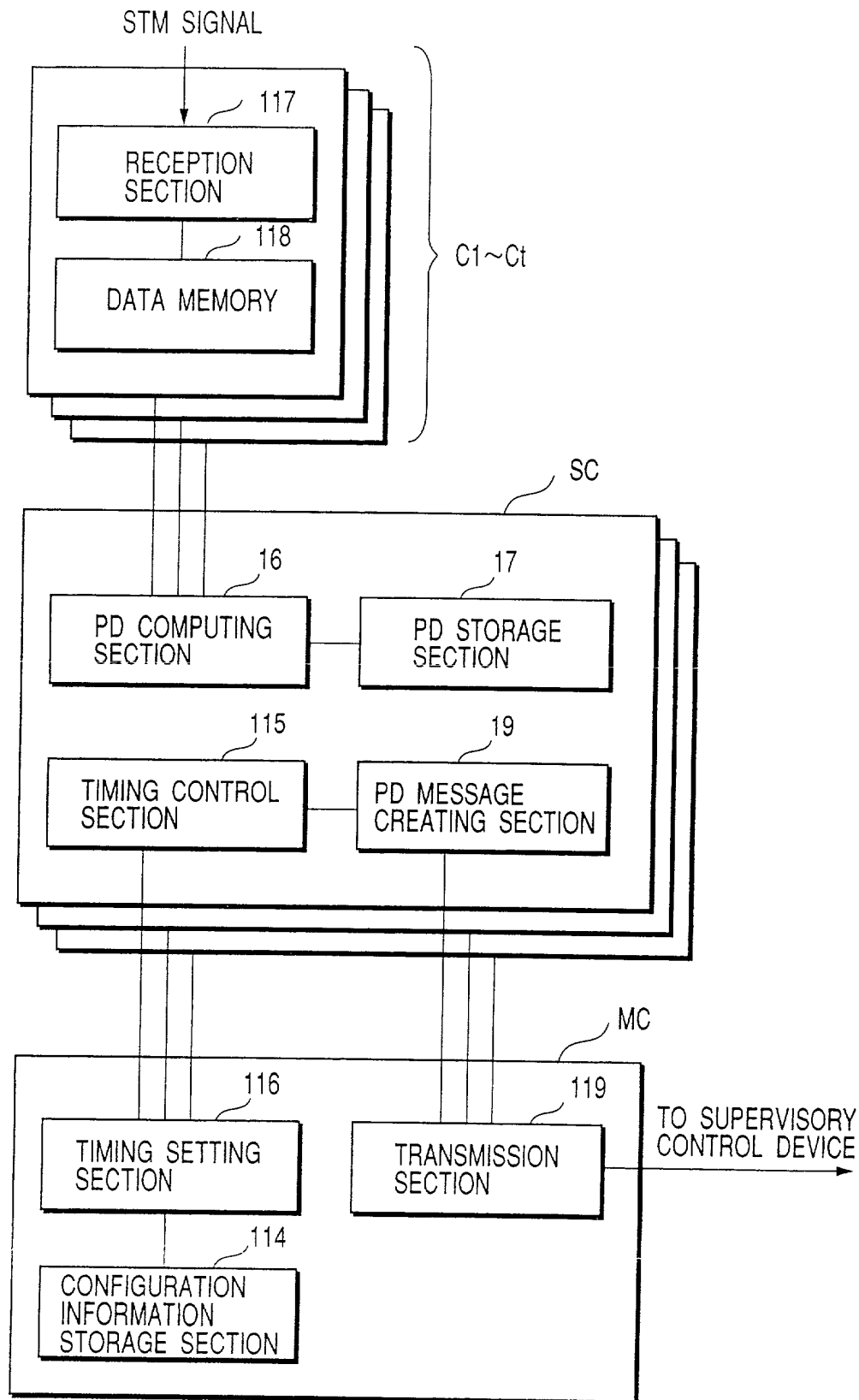


FIG.20

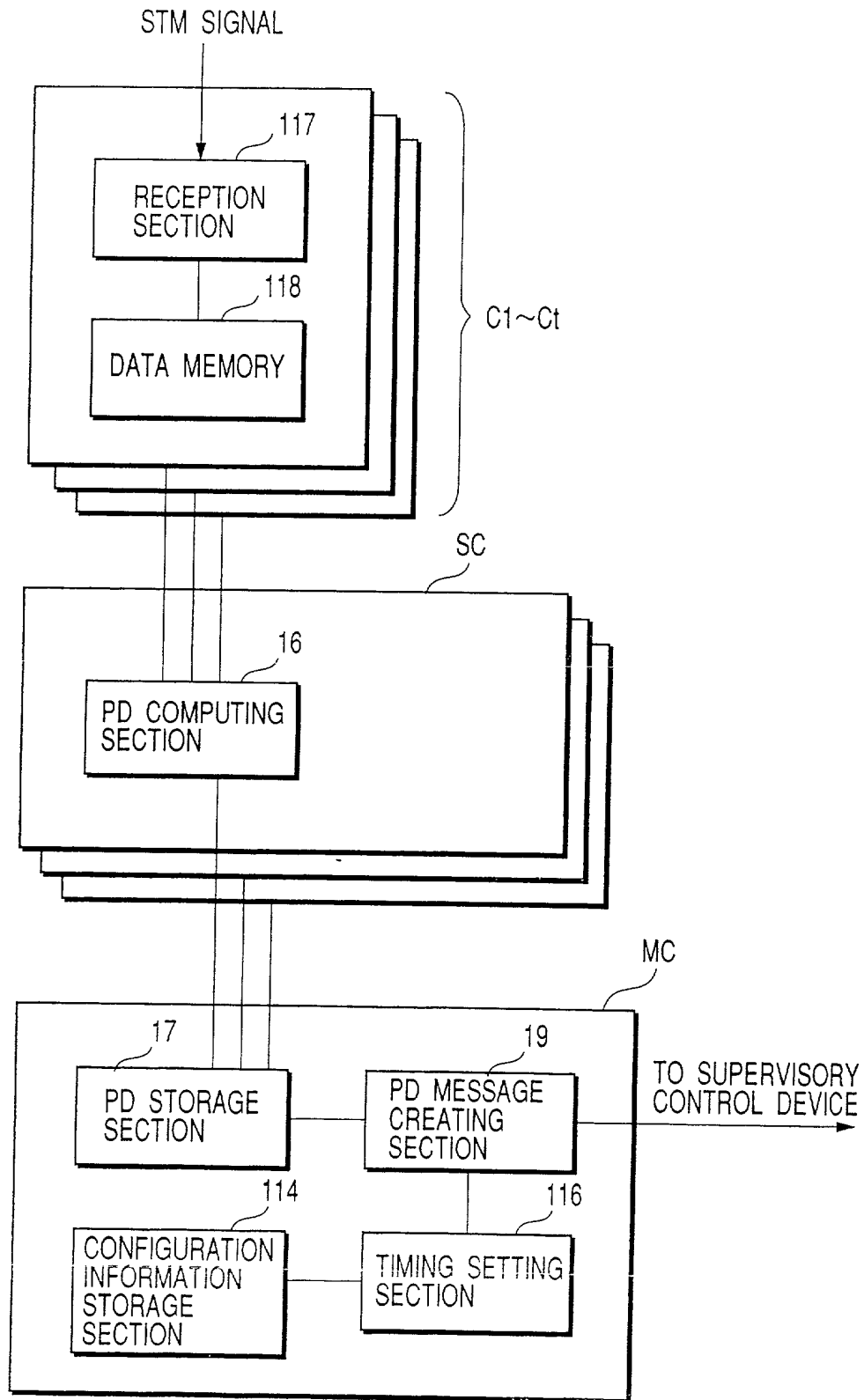


FIG. 21